

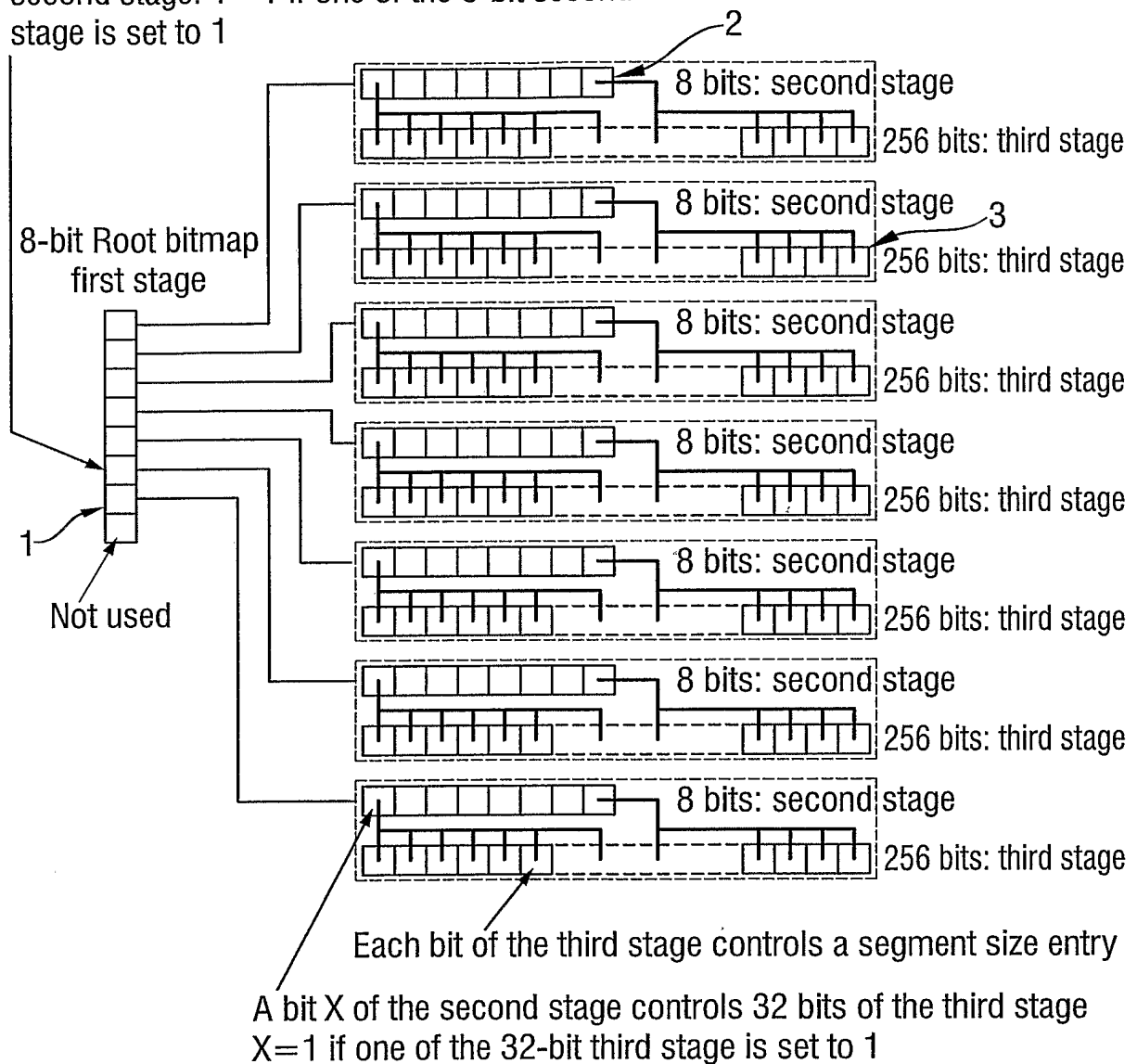
1 / 9

<64B	64-127 B	128-255 B	256-511 B	512-1K-1	1K-2K-1	2K-4K-1	4K-8K-1
<64	<128	<256	<512	<1024	<2048	<4096	<8192
1	<96	<224	<480	<992			
	2	<192	<448	<960			
		<160	<416	<928			
		4	<384	<896			
			<352	<864			
			<320	<832			
			<288	<800			
			8	<768			
				<736			
				<704			
				<672			
				<640			
				<608			
				<576			
				<544			
				16			
					<1056	<2080	<5128
					32	64	128

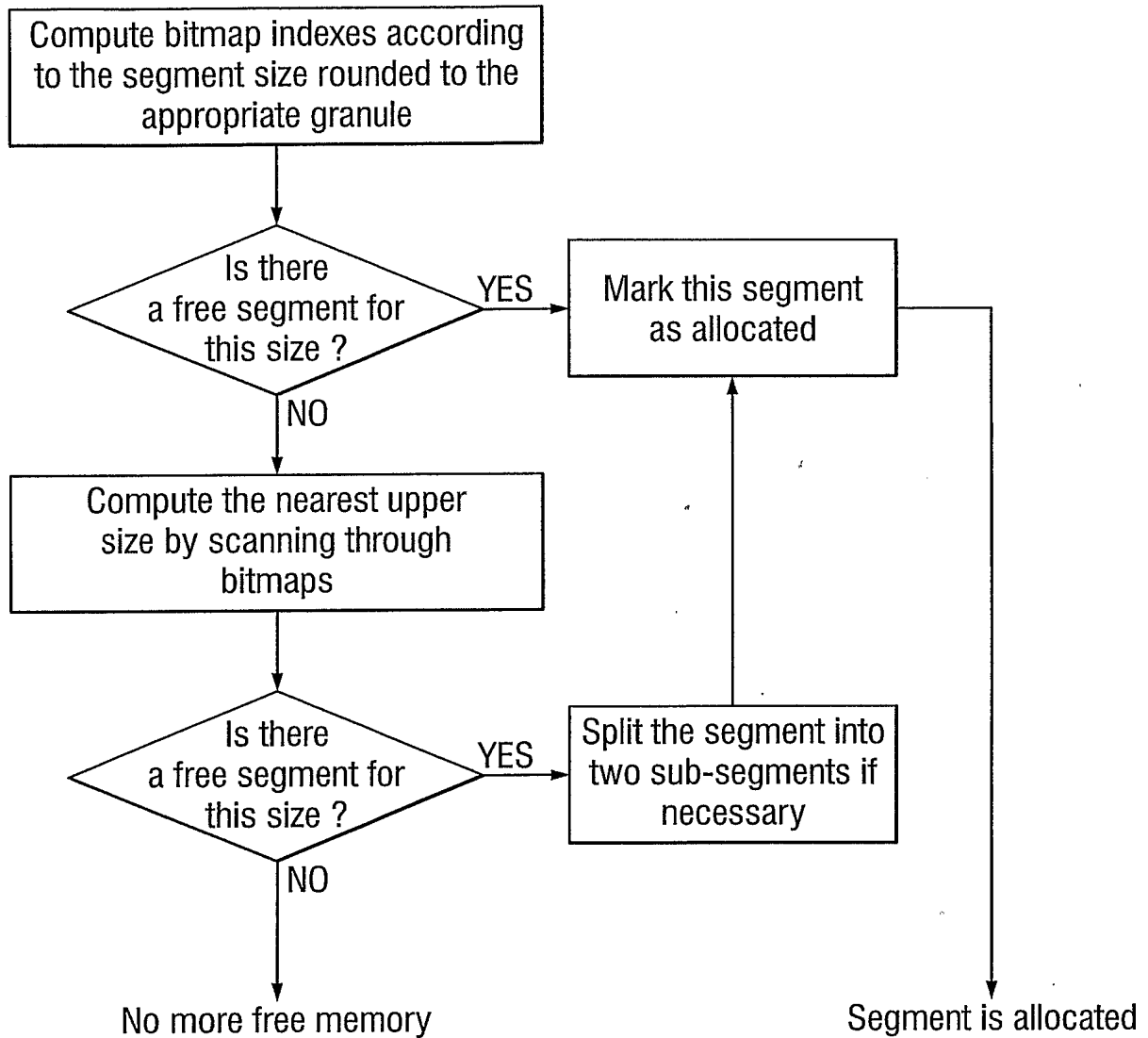
Fig. 1

2 / 9

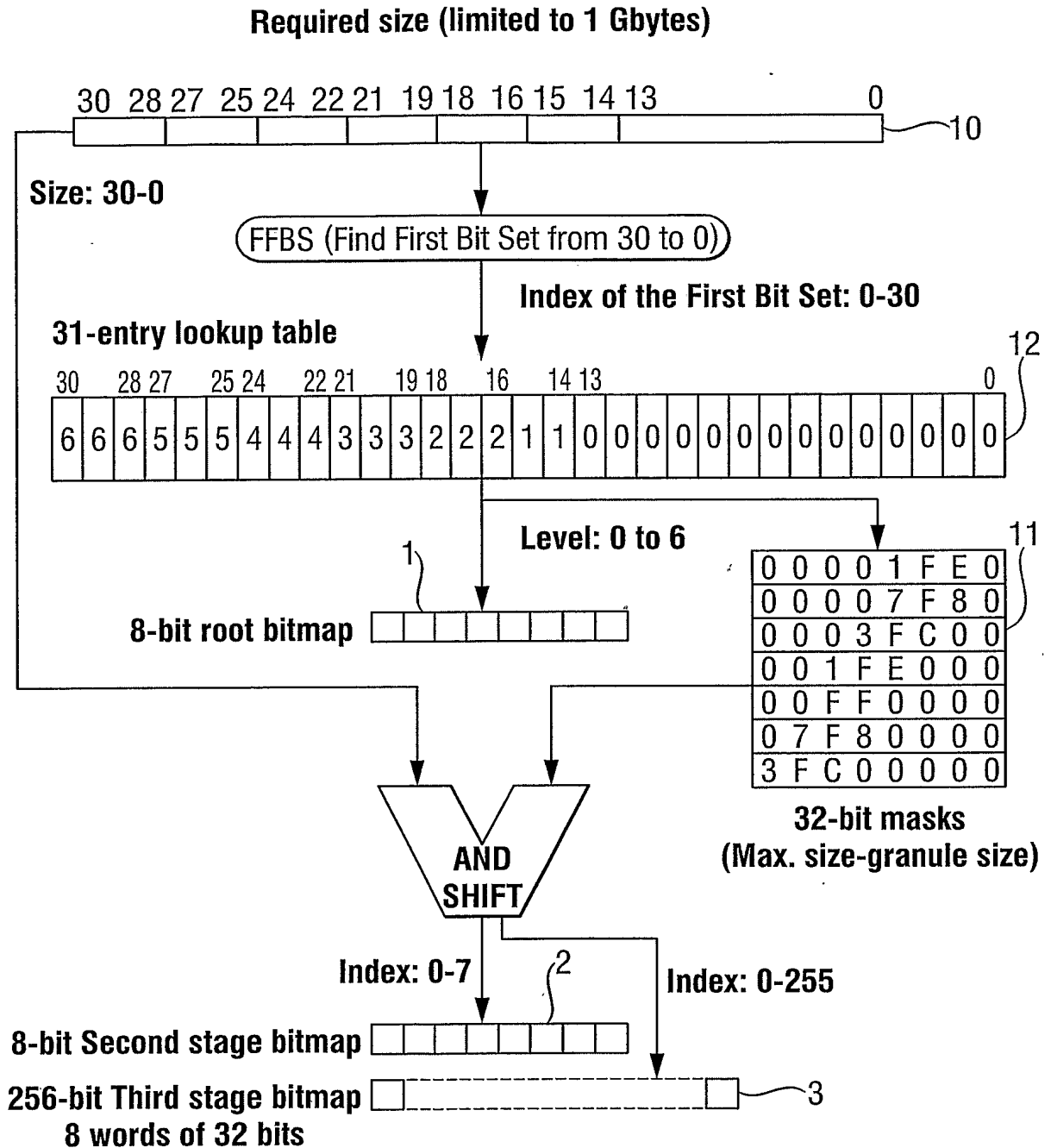
A bit Y of the first stage controls 8 bits of the second stage. Y=1 if one of the 8-bit second stage is set to 1

**Fig. 2**

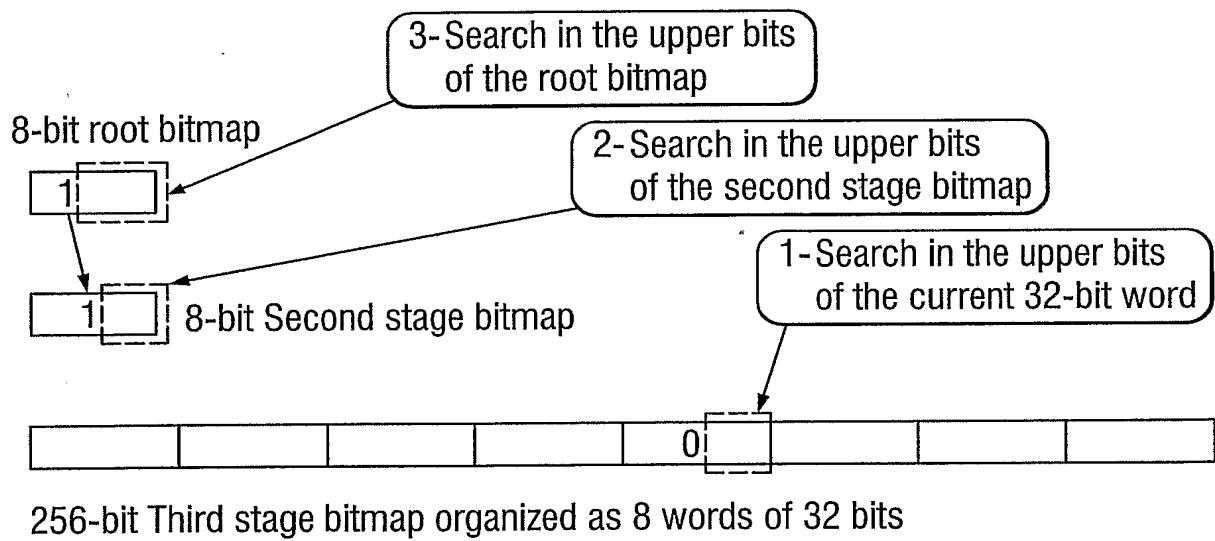
3 / 9

***Fig. 3***

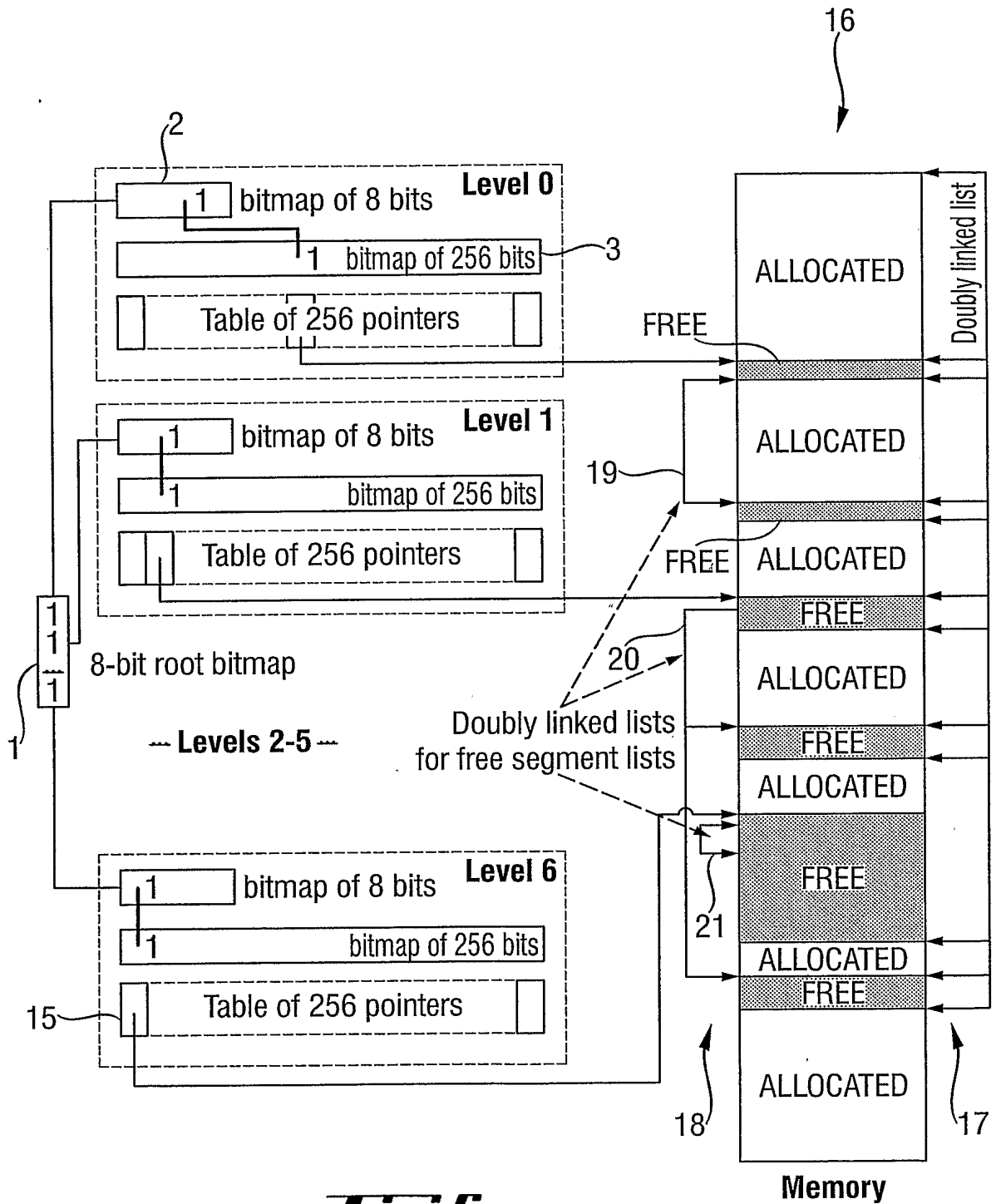
4 / 9

**Fig. 4**

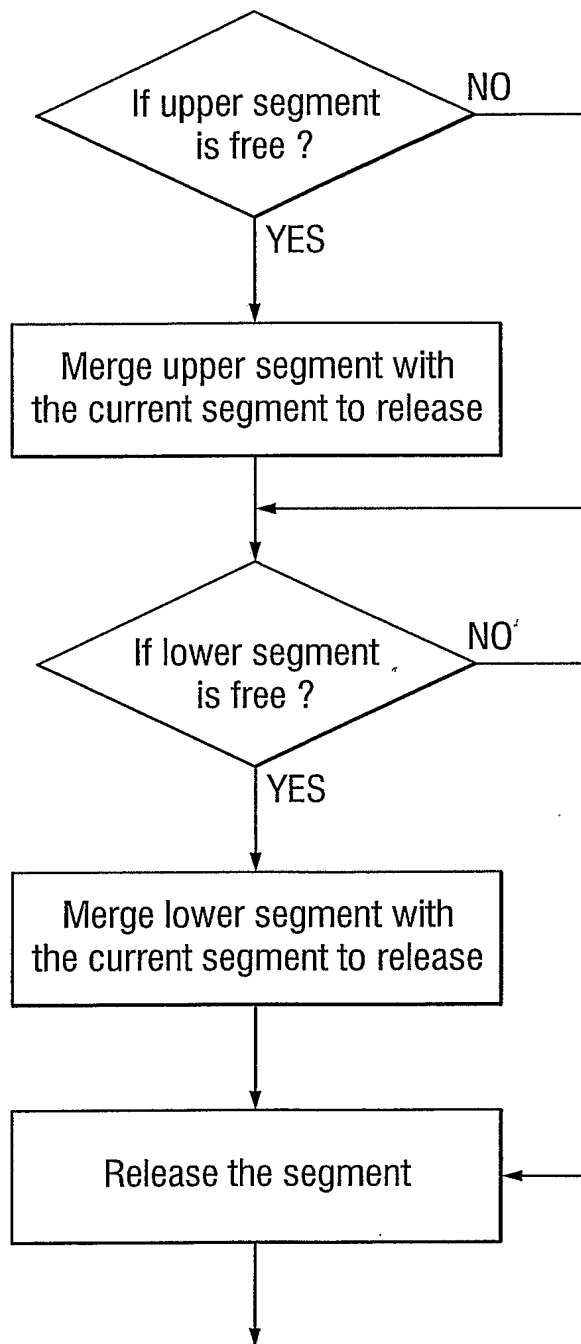
5 / 9

***Fig. 5***

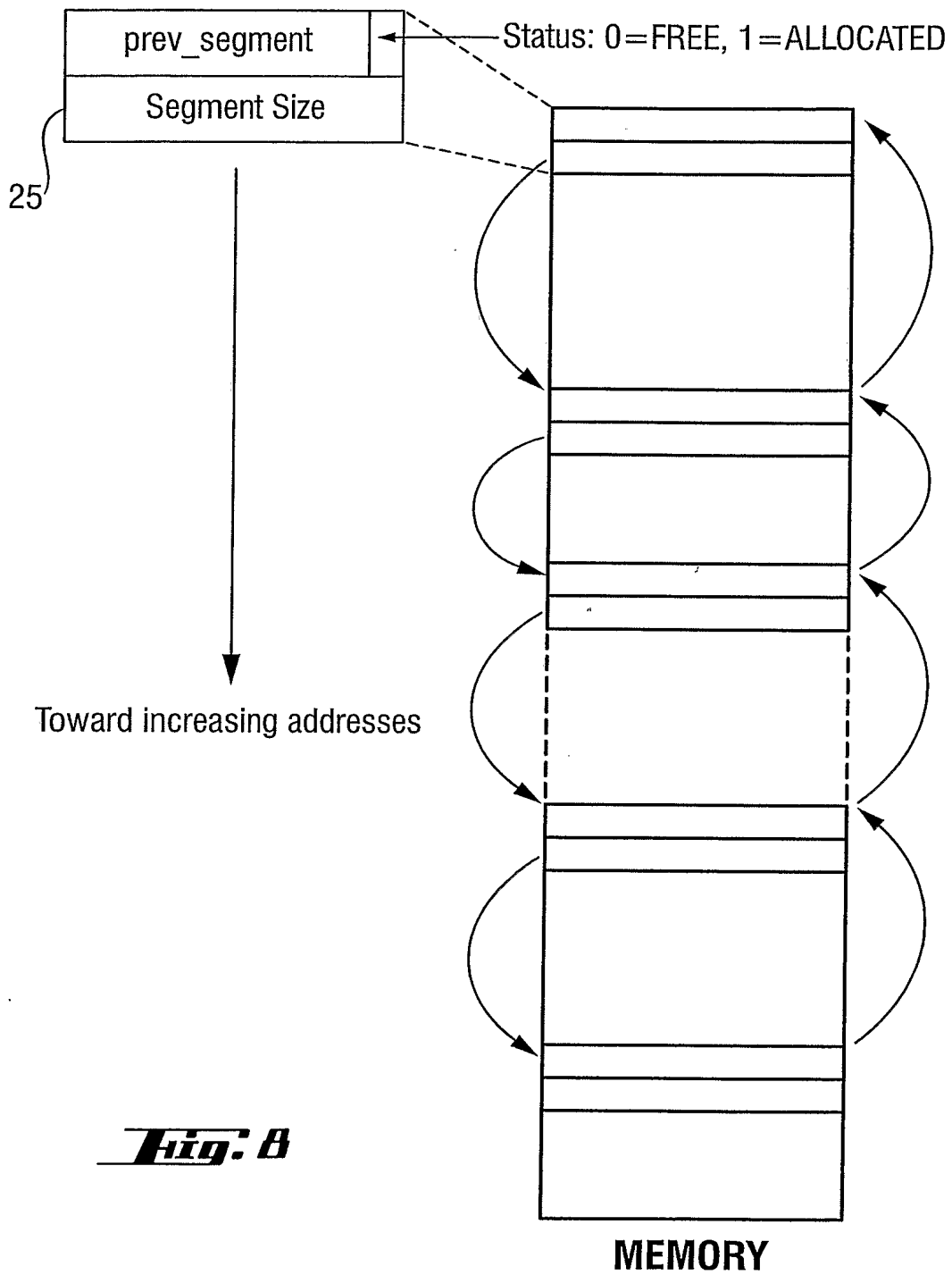
6 / 9

**Fig. 6**

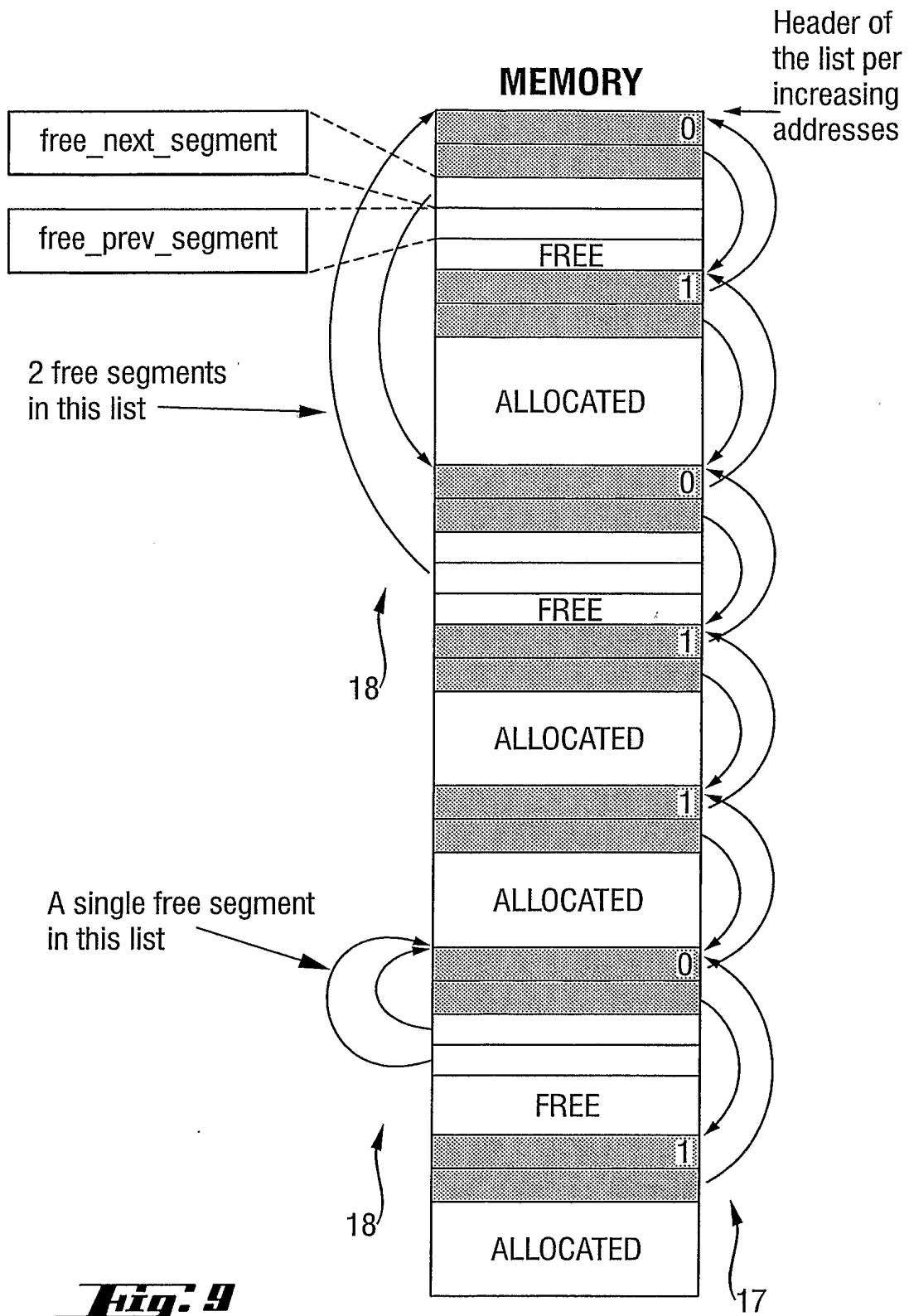
7 / 9

***Fig. 7***

8 / 9

***Fig. 8***

9 / 9

**Fig. 9**